ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention

INTEGRATED CIRCUITS HAVING POST-SILICON ADJUSTMENT CONTROL

Submision Type: Utility Patent Filing

Application Number:

10/707450

EFS ID: 52271

Server Response:

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	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application	
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First Named Applicant: Yoshiyuki Ando

Attorney Docket Number: YA06

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File Listing:

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us-fee-sheet	YA06-usfees.xml		2003-12-15				
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	application-body.dtd		2003-12-15				
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package-data	package-data.dtd		27025	2003-12-15
package-data	us-package-data.xsl		19263	2003-12-15
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